

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s) : Darmann et al.  
Serial No. : 10/531,823  
Filed : April 18, 2005  
For : Superconducting Fault Current Limiter  
Group Art Unit : 2836  
Examiner : Scott Allen Bauer

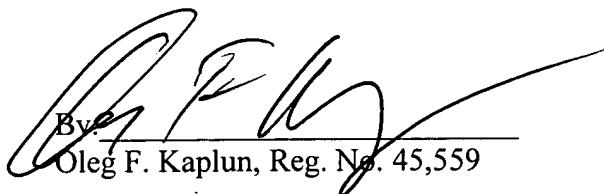
Mail Stop: Amendments  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicants hereby bring the following two (2) references to the attention of the Examiner. The references are listed on the attached modified PTO Form No. 1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
2. A copy of each Non-U.S. Patent or Non-U.S. Patent Publication listed on the Form PTO-1449, if any, is enclosed.
3. By submitting this Information Disclosure Statement, Applicants make no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

4. By submitting this Information Disclosure Statement, Applicants make no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).
5. By submitting this Information Disclosure Statement, Applicants make no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined by 35 U.S.C. §102.

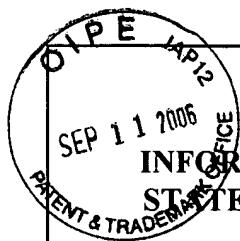
Respectfully submitted,



By: Oleg F. Kaplun, Reg. No. 45,559

Dated: September 5, 2006

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <b>FORM PTO-1449</b>	ATTY. DOCKET NO. 40121/01101	APPLICATION NO. 10/531,823
	INVENTOR(S) Darmani et al.	
	FILING DATE: April 18, 2005	

#### U. S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT/US PUBLICATION NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE*

#### FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE (M/D/Y)	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

#### OTHER DOCUMENTS

EXAMINER INITIAL		AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
		Raju et al., <i>Fault- Current Limiter with Superconducting DC Bias</i> , 1982, pp. 166-171
		Hawley et al., <i>Performance of a 1MV A High Temperature Superconductors-Enabled Saturable Magnetic Core-Type Fault Current Limiter</i> , 2004, pp. 255-259

EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	